

Searching within **The ACM Digital Library** with **Advanced Search**: (sha and hash and dual and core) ([start a new search](#))

Found 8 of 249,190

REFINE YOUR SEARCH

Refine by Keywords

Discovered Terms

Refine by People

Names
Institutions
Authors
Reviewers

Refine by Publications

Publication Year
Publication Names
ACM Publications
All Publications
Content Formats
Publishers

Refine by Conferences

Sponsors
Events
Proceeding Series

ADVANCED SEARCH

 [Advanced Search](#)

FEEDBACK

 [Please provide us with feedback](#)

Found 8 of 249,190

[Search Results](#) [Related Journals](#) [Related Magazines](#)
 [Related SIGs](#) [Related Conferences](#)

Results 1 - 8 of 8

Sort by in

 [Save results to a Binder](#)

1 [Global abstraction-safe marshalling with hash types](#)

 [James J. Leifer, Giles Peskin, Peter Sewell, Keith Wansbrough](#)

August 2003 [ICFP '03: Proceedings of the eighth ACM SIGPLAN international conference on Functional programming](#)

Publisher: ACM

Full text available:  [PDF \(226.25 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 14, Citation Count: 9

Type abstraction is a key feature of ML-like languages for writing large programs. Marshalling is necessary for writing distributed programs, exchanging values via network byte-streams or persistent stores. In this paper we combine the two, developing ...

Keywords: ML, abstract types, distributed programming, hashing, lambda calculus, marshalling, modules, programming languages, serialisation, singleton kinds, type theory

Also published in:

September 2003 [SIGPLAN Notices Volume 38 Issue 9](#)

2 [Security as a new dimension in embedded system design](#)

 [Paul Kocher, Ruby Lee, Gary McGraw, Anand Raghunathan / Srivaths Ravi](#)

June 2004 [DAC '04: Proceedings of the 41st annual conference on Design automation](#)

Publisher: ACM

Full text available:  [PDF \(209.10 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 78, Downloads (12 Months): 468, Citation Count: 19

The growing number of instances of breaches in information security in the last few years has created a compelling case for efforts towards secure electronic systems. Embedded systems, which will be ubiquitously used to capture, store, manipulate, and ...

Keywords: PDAs, architectures, battery life, cryptography, design, design methodologies, digital rights management, embedded systems, performance, security, security processing, security protocols, sensors, software attacks, tamper resistance, trusted computing, viruses

3 Technical opinion: designing cryptography for the new century

 Susan Landau

May 2000 **Communications of the ACM**, Volume 43 Issue 5

Publisher: ACM

Full text available:  [HTML](#) (35.06 KB),  [PDF](#) (215.10 KB)

Additional Information: [full citation](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 84, Citation Count: 0

4 Securing Mobile Appliances: New Challenges for the System Designer

Anand Raghunathan, Srivaths Ravi, Sunil Hattangady, Jean-Jacques Quisquater

March **DATE '03: Proceedings of the conference on Design, Automation and 2003 Test in Europe - Volume 1**, Volume 1

Publisher: IEEE Computer Society

Full text available:  [Publisher Site](#),  [PDF](#) (257.28 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 22, Downloads (12 Months): 181, Citation Count: 6

As intelligent electronic systems pervade all aspects of our lives, capturing, storing, and communicating a wide range of sensitive and personal data, security is emerging as a critical concern that must be addressed in order to enable several current ...

5 Performance Comparison of Security Mechanisms for Grid Services

Satoshi Shirasuna, Aleksander Slominski, Liang Fang, Dennis Gannon

November **GRI D '04: Proceedings of the 5th IEEE/ACM International Workshop on 2004 Grid Computing**

Publisher: IEEE Computer Society

Full text available:  [PDF](#) (145.66 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 31, Citation Count: 4

Security is one of the most important features for Grid services. There are several specifications used to add security to Grid services, and some of them have been implemented and are in use. However, since most of the security mechanisms involve slow ...

6 Security in embedded systems: Design challenges



Sriwaths Ravi, Anand Raghunathan, Paul Kocher, Sunil Hattangady

August 2004

Transactions on Embedded Computing Systems (TECS) , Volume 3

Issue 3

Publisher: ACM

Full text available: [PDF](#) (3.67 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#), [review](#)

Bibliometrics: Downloads (6 Weeks): 142, Downloads (12 Months): 1046, Citation Count: 23

Many modern electronic systems--including personal computers, PDAs, cell phones, network routers, smart cards, and networked sensors to name a few--need to access, store, manipulate, or communicate sensitive information, making security a serious concern ...

Keywords: Embedded systems, architecture, authentication, battery life, cryptographic algorithms, decryption, encryption, hardware design, processing requirements, security, security attacks, security protocols, tamper resistance

7 Energy characterization of a tiled architecture processor with on-chip networks



Jason Sungtae Kim, Michael Bedford Taylor, Jason Miller, David Wentzlaff

August 2003

ISLPED '03: Proceedings of the 2003 international symposium on Low power electronics and design

Publisher: ACM

Full text available: [PDF](#) (309.58 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 44, Citation Count: 16

Tiled architectures provide a paradigm for designers to turn silicon resources into processors with burgeoning quantities of programmable functional units and memories. The architecture has a dual responsibility: first, it must expose these resources ...

Keywords: power, raw microprocessor, scalar operand network, tile

8 Architectural Support for High Speed Protection of Memory Integrity and Confidentiality in Multiprocessor Systems

Weidong Shi, Hsien-Hsin S. Lee, Mrinmoy Ghosh, Chenghui Lu

September 2004

PACT '04: Proceedings of the 13th International Conference on Parallel Architectures and Compilation Techniques

Publisher: IEEE Computer Society

Full text available: [PDF](#) (255.33 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 18, Downloads (12 Months): 61, Citation Count: 3

Recently there is a growing effort in both the architecture and the security community to create a hardware solution for authenticating system memory. As shown in the previous work, hardware-based memory authentication will become a vital component for ...

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2009 ACM, Inc.

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)

Useful downloads:  [Adobe Acrobat](#)  [QuickTime](#)  [Windows Media Player](#)  [Real Player](#)